

# LINE MATCHING TRANSFORMER

P5122

#### **Features**

- \* Lead-free (Pb-free)
- \* RoHS compliant
- \* 11mm (0.43") seated height
- \* Industry Standard Pinout
- \* IEC 60950 and UL 60950 Certified
- \* High thermal stability

## **Applications**

- \* Set Top Boxes
- \* Telecommunications
- \* Voice
- \* Least-cost routers
- \* V.22bis

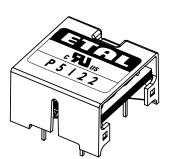
## **DESCRIPTION**

P5122 is intended for low-cost, low-speed data and voice applications. It is a direct substitute for industry standard P3189, being mechanically compatible and having identical signal/distortion and matching characteristics, and is suitable for supplementary insulation, 250V working.

P5122 uses patented design and construction methods to achieve excellent signal performance and safety isolation to international standards. P5122 is certified to IEC 60950 and UL 60950. P5122 ia a UL Recognized Component and is supported by an IEC CB certificate. The part is completely lead-free, compliant with RoHS Directive 2002/95/EC, and suitable for lead-free and conventional processing.



Patented





# SPECIFICATIONS

#### **Electrical**

At T = 25°C and as circuit fig. 2 unless otherwise stated.

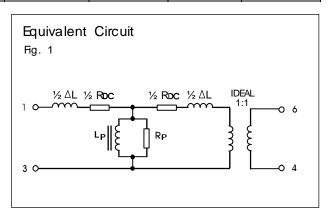
Parameter	Conditions	Min	Тур	Max	Units
Insertion Loss	$f = 2kHz$ , $R_L = 560\Omega$	-	-	1.5	dB
Frequency response	200Hz - 4kHz	-	-	±0.2	dB
Return Loss	200Hz - 4kHz	18	-	-	dB
Third Harmonic Distortion (1)	300Hz - 6dBm 697Hz - 6dBm	-	- -	-62 -70	dBm dBm
Voltage isolation (2)	50Hz DC	2.12 3.0	-	- -	kVrms kV
Operating range: Functional Storage	Ambient temperature	0 -40	-	+70 +85	°C °C

Lumped equivalent circuit parameters as Fig. 1

DC resistance, R <sub>DC</sub> <sup>(3)</sup>	Sum of windings	124	-	152	Ω
Leakage inductance ∆L		14.5	-	18	mH
Shunt inductance Lp (4)	10mV 200Hz	1.4	-	-	Н
Shunt loss Rp (4)	10mV 200Hz	4	-	-	kΩ

#### **Notes**

- Third harmonic typically exceeds other harmonics by 20dB.
   Components are 100% tested at 3.25kV DC.
- 3. Caution: do not pass DC through windings. Telephone line current, etc. must be diverted using choke or semiconductor line hold circuit.
- 4. At signal levels greater than 100mV, Lp will increase and Rp will decrease slightly but the effect is usually favourable to the return loss characteristic.



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### MATCHING RECOMMENDATIONS

#### $600\Omega$ MATCH

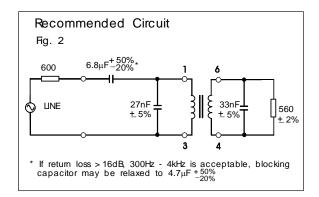
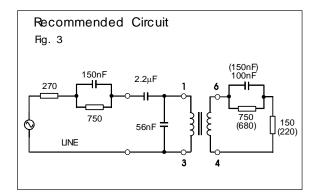
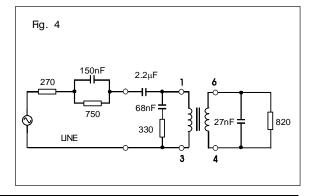


Figure 3 gives flat RX and TX responses against the CTR21 complex reference impedance (typically around ±0.5dB 300Hz - 3.4kHz). Return loss is typically better than 20dB. The circuit values in parentheses give optimum flatness at a small sacrifice in return loss.

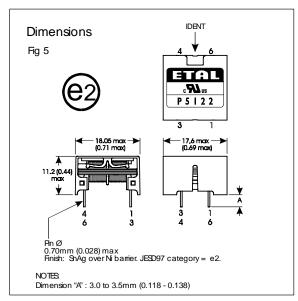
For circuits with existing board drillings, figure 4 gives good return loss (>20dB) and RX flatness against the CTR21 complex reference impedance, but TX flatness is degraded by the use of this topology.

#### **EUROPEAN CTR21 COMPLEX MATCH**





### CONSTRUCTION



Connections

Fig. 6

12.7

(0.50)

7.62

(0.30)

Your from underside 4

Tolerance ±0.3mm (±0.012inch)
Recommended PCB hole size 1mm Ø(0.04inch)

Dimensions shown are in millimetres (inches).

Geometric centres of outline and pin grid coincide within a tolerance circle of 0.6mm Windings may be used interchangeably as primary or secondary.



### SAFETY

Constructed in accordance with IEC 60950-1, EN 60950-1 and UL 60950-1, supplementary insulation, 250Vrms maximum working voltage, flammability class V-0.

## CERTIFICATION

Certified under the IEC CB scheme (Certificate GB605W) to IEC 60950 Third Edition 1999 subclauses 1.5, 1.5.1, 1.5.2, 2.9, 2.9.1, 2.9.2, 2.9.3, 2.9.4, 2.9.5, 2.10, 2.10.1, 2.10.2, 2.10.3, 2.10.3.1, 2.10.3.2, 2.10.4, 2.10.5, 2.10.5.1, 2.10.5.4, 4.7, 4.7.3, 4.7.3.1, 4.7.3.4, (Flammability Class V-0) ,5.2, 5.2.1, and 5.2.2 for a maximum working voltage of 250Vrms, nominal mains supply voltage not exceeding 300Vrms and a maximum operating temperature of +85°C in Pollution Degree 2 environment, supplementary insulation for a primary circuit.

Recognized under the Component Recognition Program of Underwriters Laboratories Inc. to US and Canadian requirements CAN/CSA C22.2 No. 60950-1-03/UL60950-1, First Edition, based on IEC 60950-1, First Edition, maximum working voltage 250Vrms, Pollution Degree 2, supplementary insulation.

UL File number E203175.

Additionally, Profec Technologies certifies all transformers as providing voltage isolation of 2.12kVrms, 3kV DC minimum. All shipments are supported by a certificate of conformity to current applicable safety standards.

#### ABSOLUTE MAXIMUM RATINGS

(Ratings of components independent of circuit).

 $\begin{array}{lll} \text{Short term isolation voltage (1s)} & 2.12 \text{kVrms} \\ & 3.0 \text{kVDC} \\ \text{DC current} & 100 \mu \text{A} \\ \text{Storage temperature} & -40 ^{\circ} \text{C to} \\ & +85 ^{\circ} \text{C} \end{array}$ 

Lead temperature, 10s 260°C

### INTELLECTUAL PROPERTY RIGHTS

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P5122 design and construction are protected by patents.

USA Patent No. 6,690,254. Other patents pending.

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