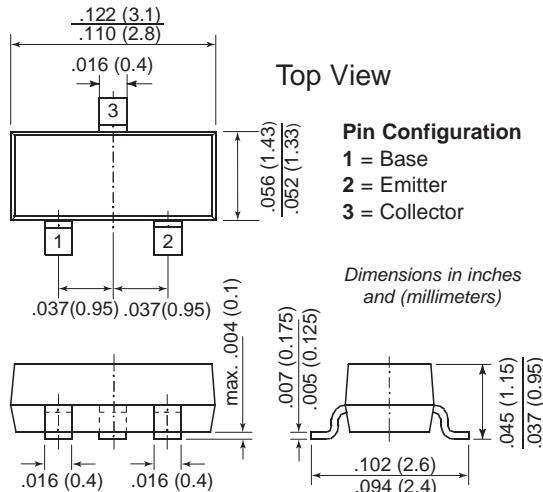


Small Signal Transistors (NPN)


TO-236AB (SOT-23)


Mechanical Data

Case: SOT-23 Plastic Package

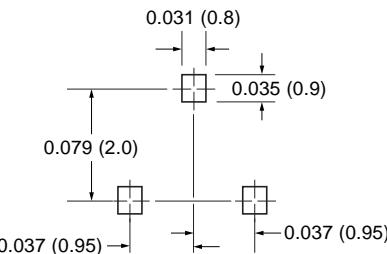
Weight: approx. 0.008g

Packaging Codes/Options:

E8/10K per 13" reel (8mm tape), 30K/box

E9/3K per 7" reel (8mm tape), 30K/box

Mounting Pad Layout



Type	Marking	Type	Marking
BC846A	1A	BC848A	1J
B	1B	B	1K
BC847A	1E	C	1L
B	1F	BC849B	2B
C	1G	C	2C

Features

- NPN Silicon Epitaxial Planar Transistors for switching and AF amplifier applications.
- Especially suited for automatic insertion in thick and thin-film circuits.
- These transistors are subdivided into three groups (A, B, and C) according to their current gain. The type BC846 is available in groups A and B, however, the types BC847 and BC848 can be supplied in all three groups. The BC849 is a low noise type available in groups B and C. As complementary types, the PNP transistors BC856...BC859 are recommended.

Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector-Base Voltage	VCBO	80	V
		50	
		30	
Collector-Emitter Voltage	VCES	80	V
		50	
		30	
Collector-Emitter Voltage	VCEO	65	V
		45	
		30	
Emitter-Base Voltage	VEBO	6	V
		5	
Collector Current	Ic	100	mA
Peak Collector Current	ICM	200	mA
Peak Base Current	IBM	200	mA
Peak Emitter Current	-IEM	200	mA
Power Dissipation at TSB = 50°C	Ptot	310 ⁽¹⁾	mW
Thermal Resistance Junction to Ambiant Air	R _{θJA}	450 ⁽¹⁾	°C/W
Thermal Resistance Junction to Substrate Backside	R _{θSB}	320 ⁽¹⁾	°C/W
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _s	-65 to +150	°C

Note: (1) Device on fiberglass substrate, see layout on third page.

Small Signal Transistors (NPN)
Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Small Signal Current Gain Current Gain Group A B C	h_{fe}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$ $f = 1\text{kHz}$	—	220	—	—
			—	330	—	—
			—	600	—	—
Input Impedance Current Gain Group A B C	h_{ie}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$ $f = 1\text{kHz}$	1.6	2.7	4.5	$\text{k}\Omega$
			3.2	4.5	8.5	
			6.0	8.7	15.0	
Output Admittance Current Gain Group A B C	h_{oe}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$ $f = 1\text{kHz}$	—	18	30	μS
			—	30	60	
			—	60	110	
Reverse Voltage Transfer Ratio Current Gain Group A B C	h_{re}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$ $f = 1\text{kHz}$	—	$1.5 \cdot 10^{-4}$	—	—
			—	$2 \cdot 10^{-4}$	—	—
			—	$3 \cdot 10^{-4}$	—	—
DC Current Gain Current Gain Group A B C Current Gain Group A B C	h_{FE}	$V_{CE} = 5\text{V}, I_C = 10\mu\text{A}$	—	90	—	—
			—	150	—	—
			—	270	—	—
	h_{FE}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$	110	180	220	—
			200	290	450	—
			420	520	800	—
Collector Saturation Voltage	V_{CEsat}	$I_C = 10\text{mA}, I_B = 0.5\text{mA}$	—	90	250	mV
		$I_C = 100\text{mA}, I_B = 5\text{mA}$	—	200	600	
Base Saturation Voltage	V_{BEsat}	$I_C = 10\text{mA}, I_B = 0.5\text{mA}$	—	700	—	mV
		$I_C = 100\text{mA}, I_B = 5\text{mA}$	—	900	—	
Base-Emitter Voltage	V_{BEon}	$V_{CE} = 5\text{V}, I_C = 2\text{mA}$	580	660	700	mV
		$V_{CE} = 5\text{V}, I_C = 10\text{mA}$	—	—	770	
Collector-Base Cutoff Current	I_{CBO}	$V_{CB} = 30\text{V}$	—	—	15	nA μA
		$V_{CB} = 30\text{V}, T_J = 150^\circ\text{C}$	—	—	5	
Gain-Bandwidth Product	f_T	$V_{CE} = 5\text{V}, I_C = 10\text{mA}$ $f = 100\text{MHz}$	—	300	—	MHz
Collector-Base Capacitance	C_{CBO}	$V_{CB} = 10\text{V}, f = 1\text{MHz}$	—	3.5	6	pF
Emitter-Base Capacitance	C_{EBO}	$V_{EB} = 0.5\text{V}, f = 1\text{MHz}$	—	9	—	pF
Noise Figure BC846, BC847, BC848 BC849 BC849	F	$V_{CE} = 5\text{V}, I_C = 200\mu\text{A}$ $R_G = 2\text{k}\Omega, f = 1\text{kHz}, \Delta f = 200\text{Hz}$	—	2	10	dB
		$V_{CE} = 5\text{V}, I_C = 200\mu\text{A}$ $R_G = 2\text{k}\Omega, f = 30...15000\text{Hz}$	—	1.2	4	
			—	1.4	4	dB

Note: (1) Device on fiberglass substrate, see layout on next page

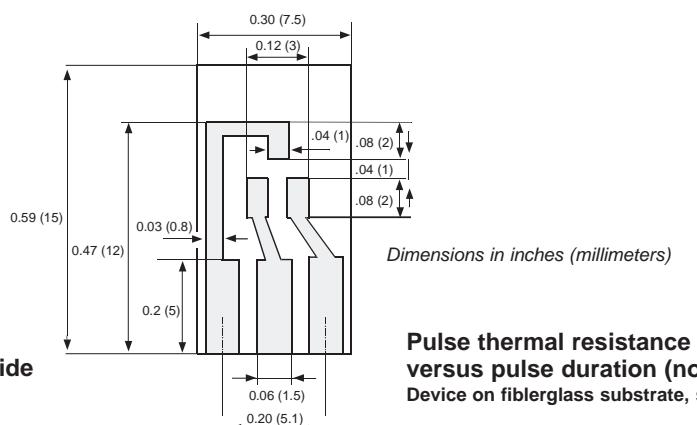
Small Signal Transistors (NPN)

Layout for $R_{\Theta JA}$ test

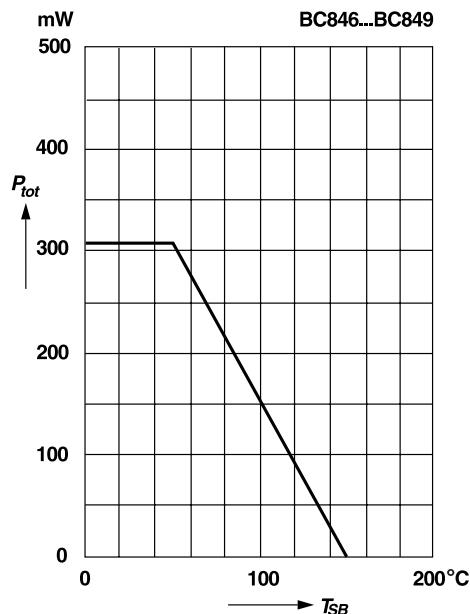
Thickness:

Fiberglass 0.059 in. (1.5 mm)

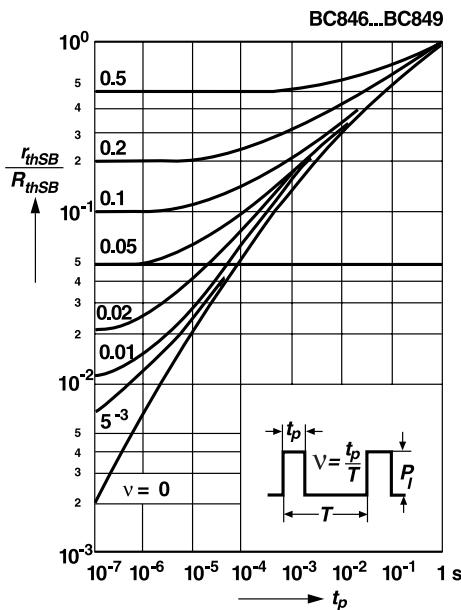
Copper leads 0.012 in. (0.3 mm)



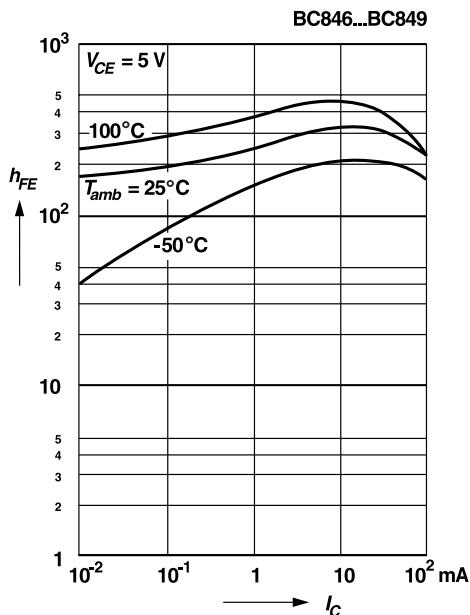
Admissible power dissipation versus temperature of substrate backside
Device on fiberglass substrate, see layout



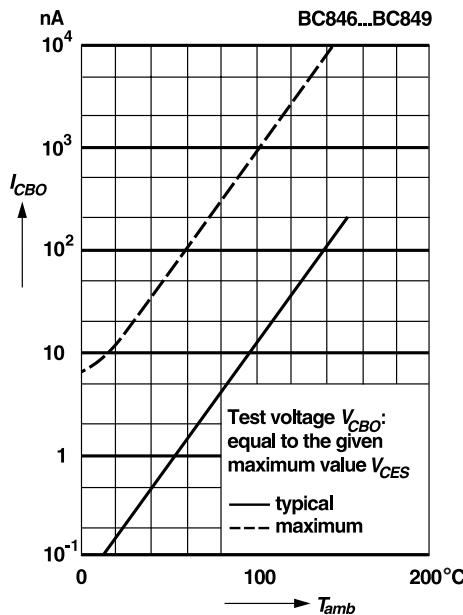
Pulse thermal resistance versus pulse duration (normalized)
Device on fiberglass substrate, see layout



DC current gain versus collector current



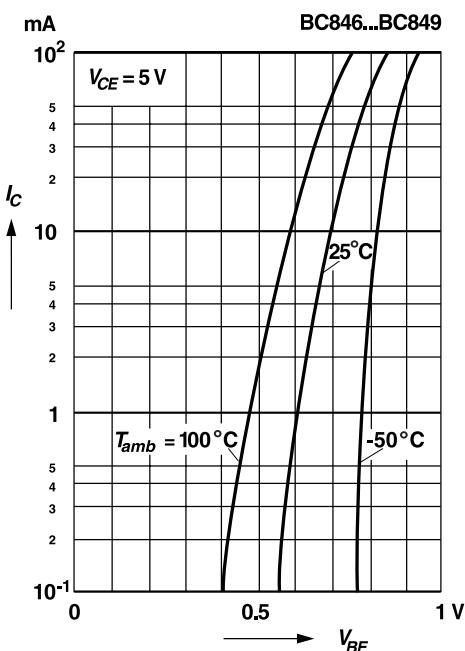
Collector-Base cutoff current versus ambient temperature



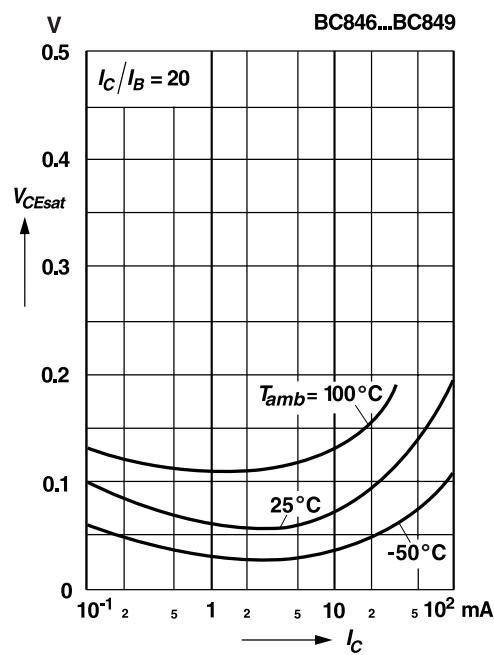
Small Signal Transistors (NPN)

Ratings and Characteristic Curves ($T_A = 25^\circ\text{C}$ unless otherwise noted)

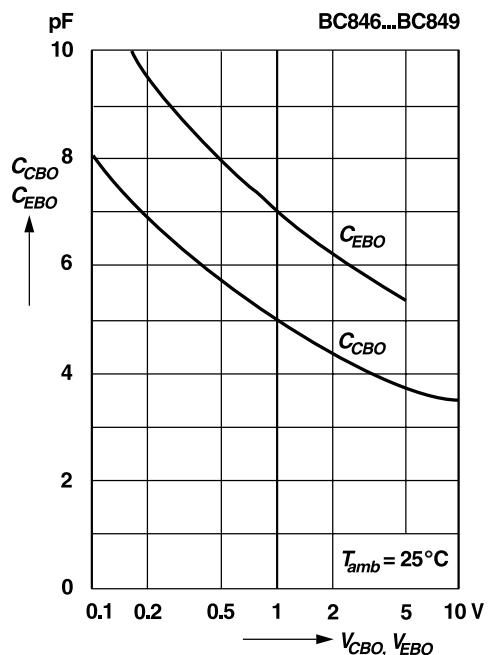
Collector current versus
base-emitter voltage



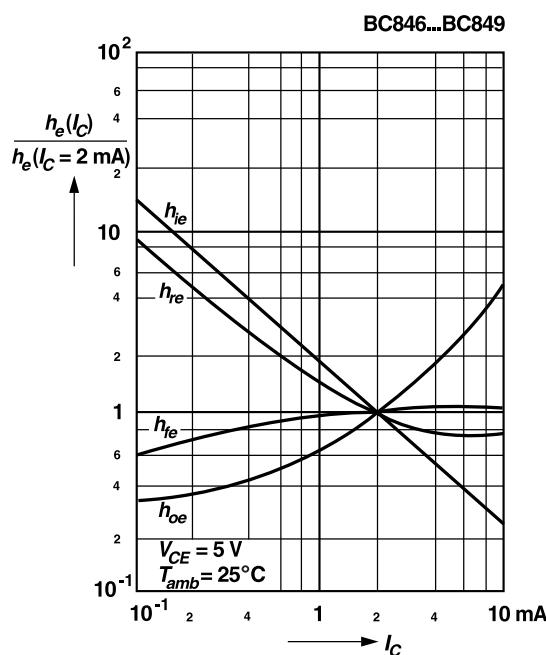
Collector saturation voltage
versus collector current



Collector base capacitance,
Emitter base capacitance
versus reverse bias voltage



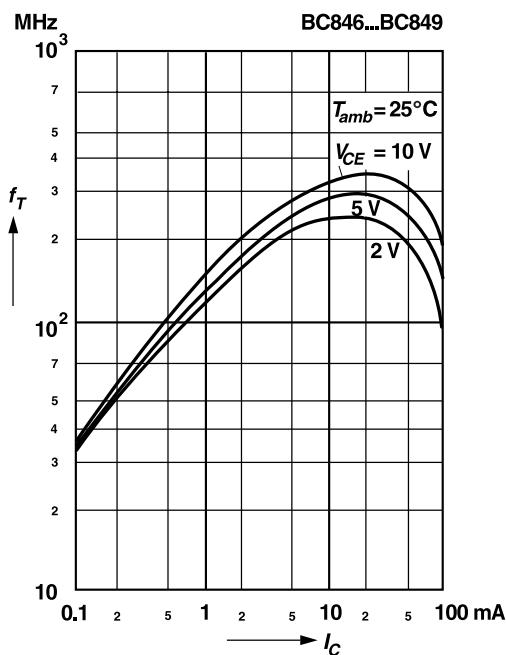
Relative h-parameters
versus collector current



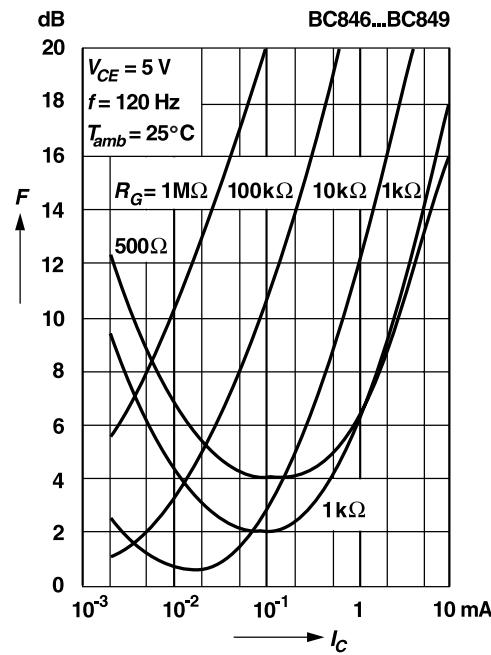
Small Signal Transistors (NPN)

Ratings and Characteristic Curves ($T_A = 25^\circ\text{C}$ unless otherwise noted)

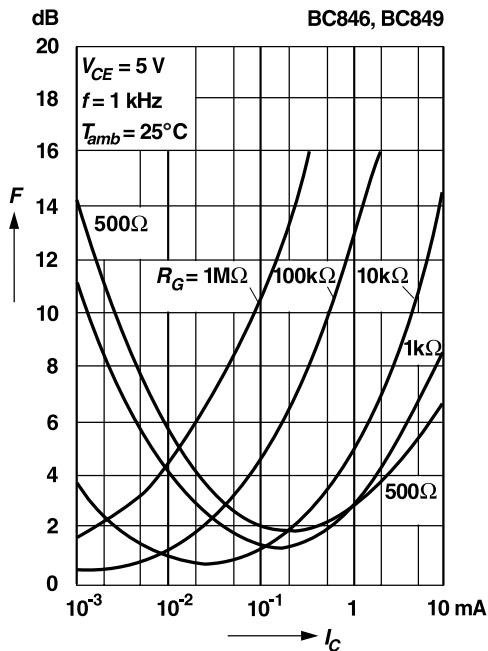
Gain-bandwidth product
versus collector current



Noise figure
versus collector current



Noise figure
versus collector current



Noise figure
versus collector emitter voltage

